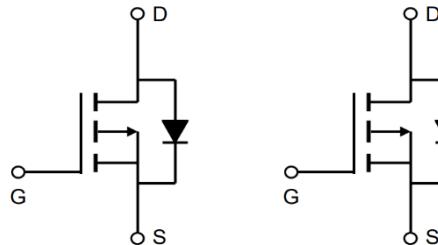


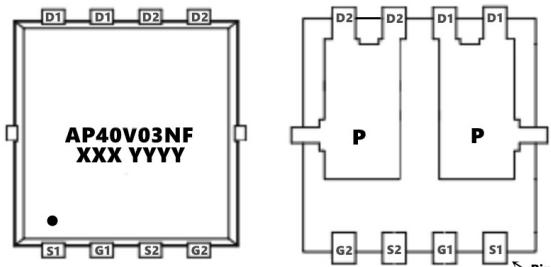
-30V P+P-Channel Enhancement Mode MOSFET
Description

The AP40V03NF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.


General Features

$V_{DS} = -30V$ $I_D = -40A$

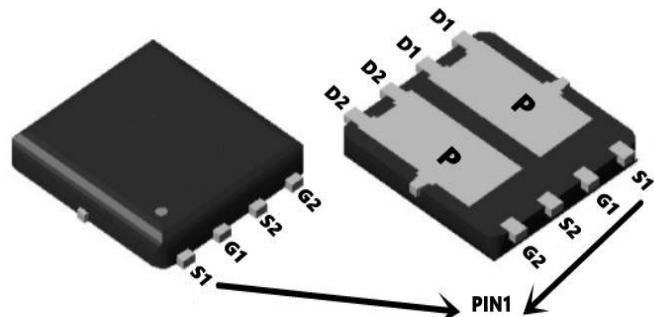
$R_{DS(ON)} < 16m\Omega$ @ $V_{GS} = -10V$ (**Type: 11.5m\Omega**)


Application

Lithium battery protection

Wireless impact

Mobile phone fast charging


Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP40V03NF	PDFN5*6-8L	AP40V03NF XXX YYYY	5000

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-30	V
VGS	Gate-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-40	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-23	A
IDM	Pulsed Drain Current ²	-120	A
EAS	Single Pulse Avalanche Energy ³	68	mJ
IAS	Avalanche Current	-29.4	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation ⁴	3.1	W
TSTG	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	25	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	2.4	°C/W

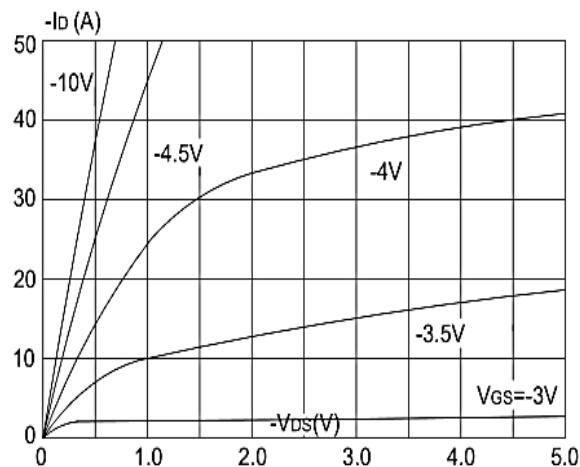
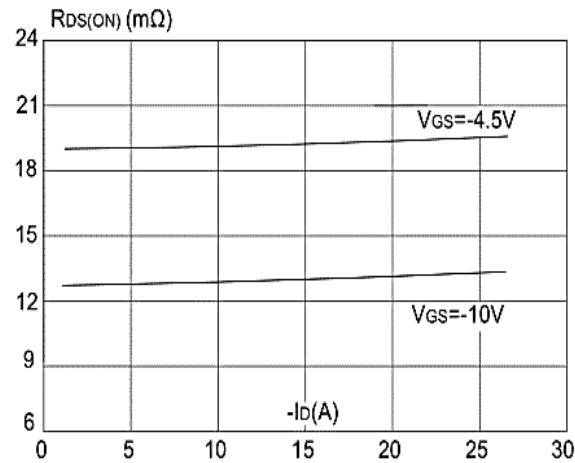
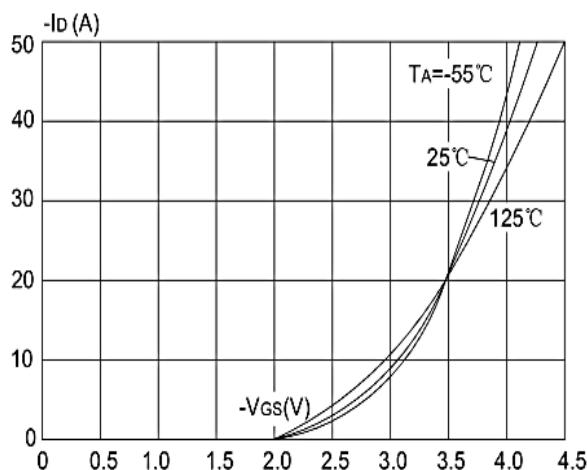
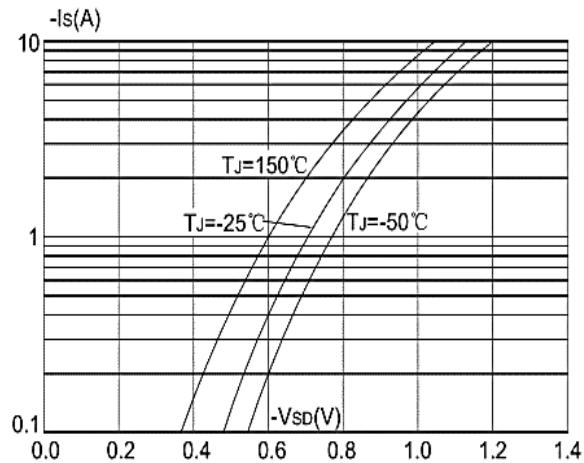
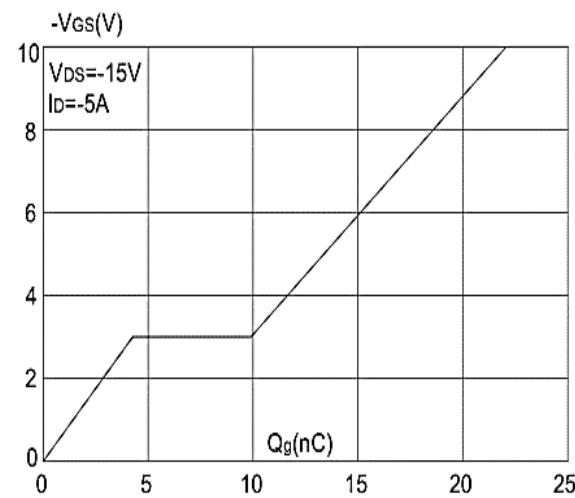
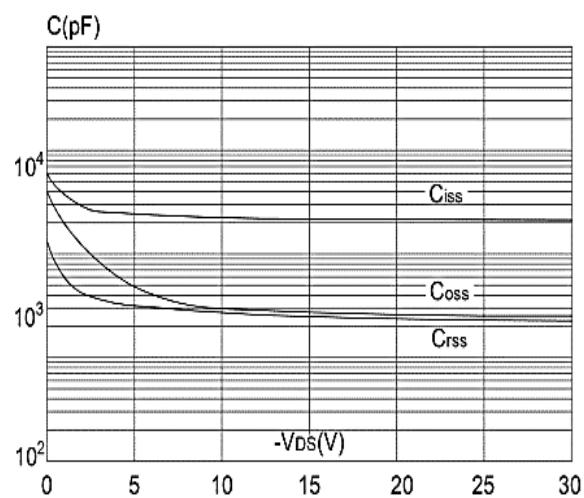


-30V P+P-Channel Enhancement Mode MOSFET
Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250μA	-30	-32.5	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} = -30V, V _{GS} =0V,	-	-	-1	μA
IGSS	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D = -250μA	-1.2	-1.5	-2.5	V
RDS(on)	Static Drain-Source on-Resistance note3	V _{GS} = -10V, I _D = -10A	-	11.5	16	mΩ
		V _{GS} = -4.5V, I _D = -5A	-	16	20	
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	4.9	7.0	9.1	Ω
C _{iss}	Input Capacitance	V _{DS} = -24V, V _{GS} =10V, f=1.0MHz	-	2130	-	pF
C _{oss}	Output Capacitance		-	280	-	pF
C _{rss}	Reverse Transfer Capacitance		-	252	-	pF
Q _g	Total Gate Charge	V _{DS} = -24V, I _D = -1A, V _{GS} = -10V	-	22	-	nC
Q _{gs}	Gate-Source Charge		-	4	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		-	5.8	-	nC
td(on)	Turn-on Delay Time	V _{DD} = -24V, I _D = -1A, V _{GS} = -10V, R _{GEN} =7.0Ω	-	9	-	ns
t _r	Turn-on Rise Time		-	13	-	ns
td(off)	Turn-off Delay Time		-	48	-	ns
t _f	Turn-off Fall Time		-	20	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-29.5	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-44	A
VSD	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S = -1A	-	-0.74	-1.2	V

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width .The EAS data shows Max. rating .
- 3、The power dissipation is limited by 175°C junction temperature
- 4、EAS condition: TJ=25°C, VDD= -24V, VG= -10V, RG=7Ω, L=0.1mH, IAS= -29.5A
- 5、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

-30V P+P-Channel Enhancement Mode MOSFET
Typical Characteristics

Figure 1: Output Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 2: Typical Transfer Characteristics

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics

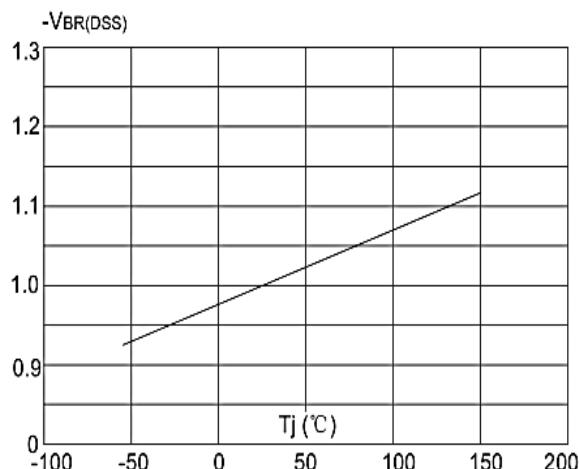
-30V P+P-Channel Enhancement Mode MOSFET


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

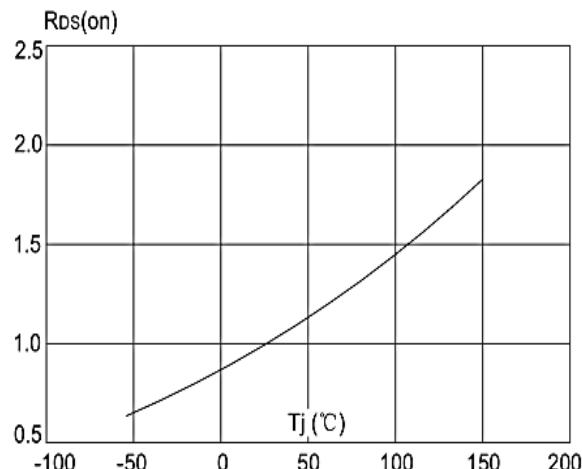


Figure 8: Normalized on Resistance vs. Junction Temperature

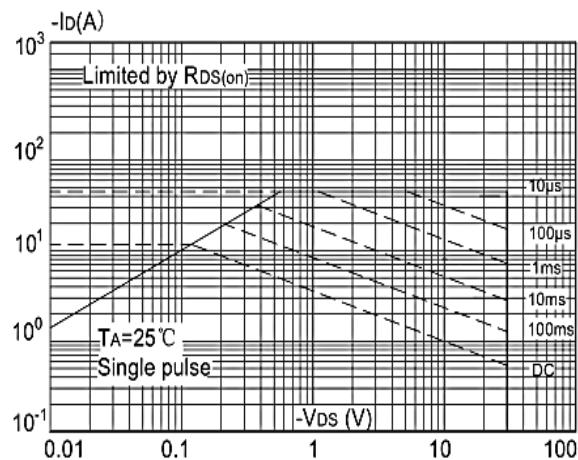


Figure 9: Maximum Safe Operating Area

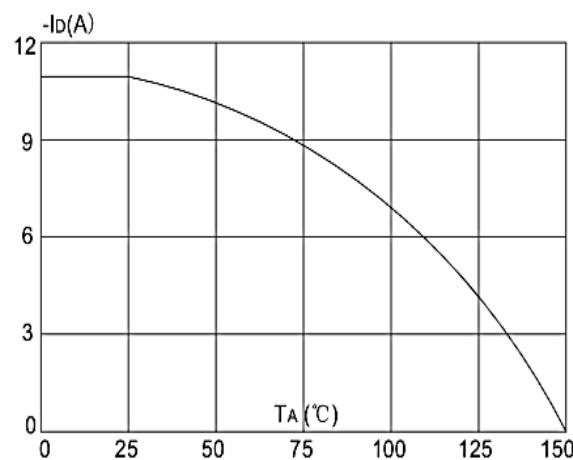


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

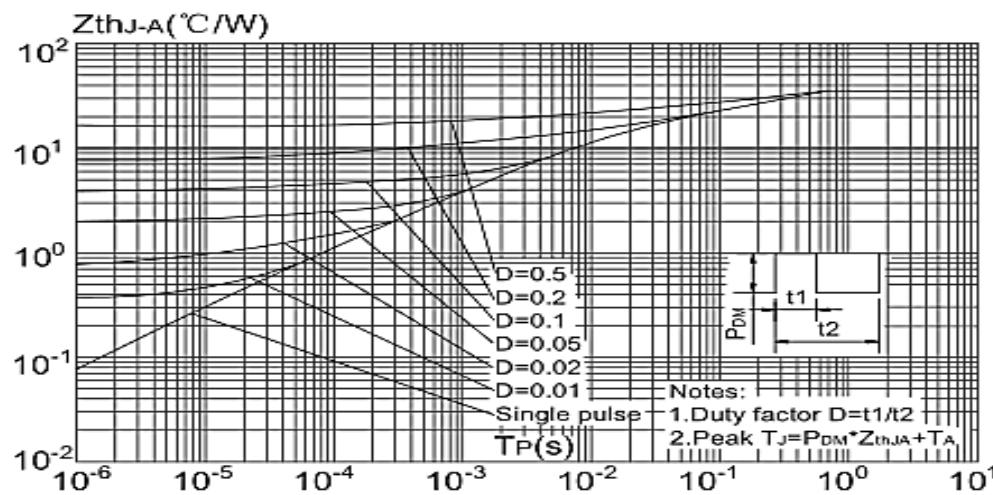
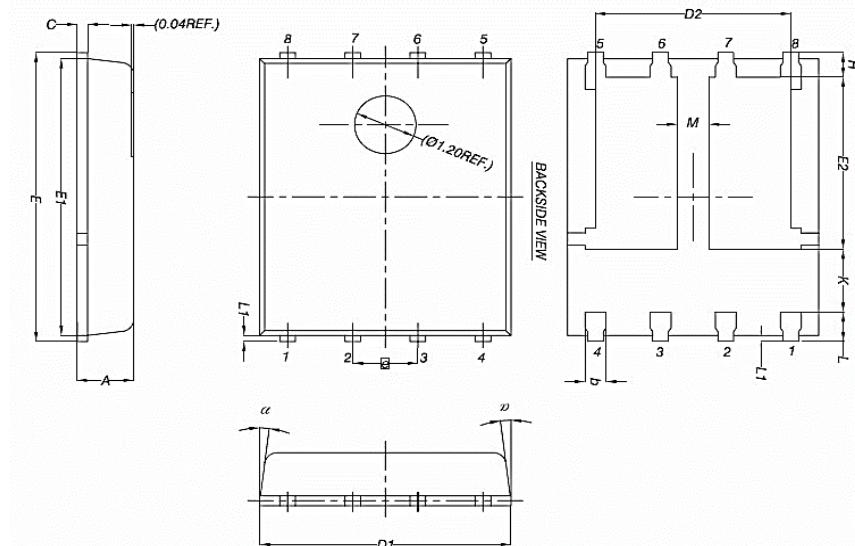


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

-30V P+P-Channel Enhancement Mode MOSFET
Package Mechanical Data-DFN5*6-8L-JQ Double



Symbol	Common		
	Mim	Nom	Max
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	3.30	3.45
E2	3.38	3.05	3.20
e	1.27BSC		
H	0.41	0.51	0.61
K	1.10	--	--
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
M	0.50	--	--
a	0°	--	12°